

REMARKS/ARGUMENTS

Applicant gratefully acknowledges the indication that claims 7-12 and 22-26 include allowable subject matter. Accordingly, claim 7 has been amended to include the subject matter of previously pending independent claim 1, and claim 22 has been amended to include the subject matter of independent claim 21. Thus it is respectfully submitted that claims 2-12 and 22-26 are patentable.

Independent claim 21 stands rejected under 35 U.S.C. §103(a) over U.S. Patent No. 5,045,993 (Murakami). Applicant respectfully traverses the rejection. As to claim 21, Murakami nowhere teaches or suggests a butterfly coprocessor coupled to a DSP to perform an operation scheduled by the DSP. In this regard, the Office Action states that signal processor 404 of Murakami corresponds to the DSP and host processor 403 corresponds to the butterfly coprocessor.

Thus there must be some teaching or suggestion in Murakami for signal processor 404 to schedule an operation for performance in host processor 403, or the rejection must fail. Because there is no such teaching or suggestion in Murakami, the rejection is improper. Instead, host processor 403 is used to schedule operations performed in signal processor 404. That is, "when the content of a process to be executed by the signal processor 404 needs to be changed, the host processor 403 gives the signal processor 404 the hold request signal 405 requesting the temporary stop of instruction word execution." Murakami, col. 36, lns. 38-42. Thus Murakami concludes that:

the operation of the signal processor [i.e., signal processor 404] is stopped temporarily *by an instruction provided by the host processor* [i.e., host processor 403] to reload the internal instruction memory of the signal processor, the program stored in the signal processor can optionally be changed after the signal processor has been fabricated, which enhances the efficiency of development activities and is highly economical.

Murakami, col. 38, lns. 34-41 (emphasis added).

This is the teaching of Murakami; host processor 403 schedules operations in signal processor 404, not vice-versa. Murakami thus teaches away from the system recited in claim 21 and accordingly, claim 21 is patentable. M.P.E.P. §2141.02.

To the extent that the Office Action appears to contend that signal processor 404 of Murakami somehow schedules performance of an operation in host processor 403, Applicant respectfully disagrees. In this regard, the Office Action states that as signal processor 404 sends

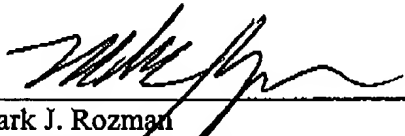
a hold authorizing signal 406 to host processor 403, somehow this teaches or suggests scheduling performance of an operation in host processor 403. Applicant respectfully disagrees. Instead, as detailed by Murakami this signal 406 is merely a status signal indicating that signal processor 404 has entered into the operation shown in the flow chart of FIG. 39 of Murakami. This flow chart is performed entirely in signal processor 404 (conceded by the Office Action, p. 6). Murakami, col. 36, lns. 35-37. There is nothing in Murakami that anywhere teaches or suggests that any part of this method is performed in host processor 403. In fact there is no teaching in Murakami as to any operation performed in host processor 403, let alone an operation scheduled by signal processor 404. Accordingly, independent claim 21 is patentable over Murakami.

In view of these remarks, the application is now in condition for allowance and the Examiner's prompt action in accordance therewith is respectfully requested. The Commissioner is authorized to charge any additional fees or credit any overpayment to Deposit Account No. 20-1504.

Respectfully submitted,

Date: _____

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Mark J. Rozman
Registration No. 42,117
TROP, PRUNER & HU, P.C.
1616 S. Voss Road, Suite 750
Houston, Texas 77057-2631
(512) 418-9944 [Phone]
(713) 468-8883 [Fax]
Customer No.: 21906